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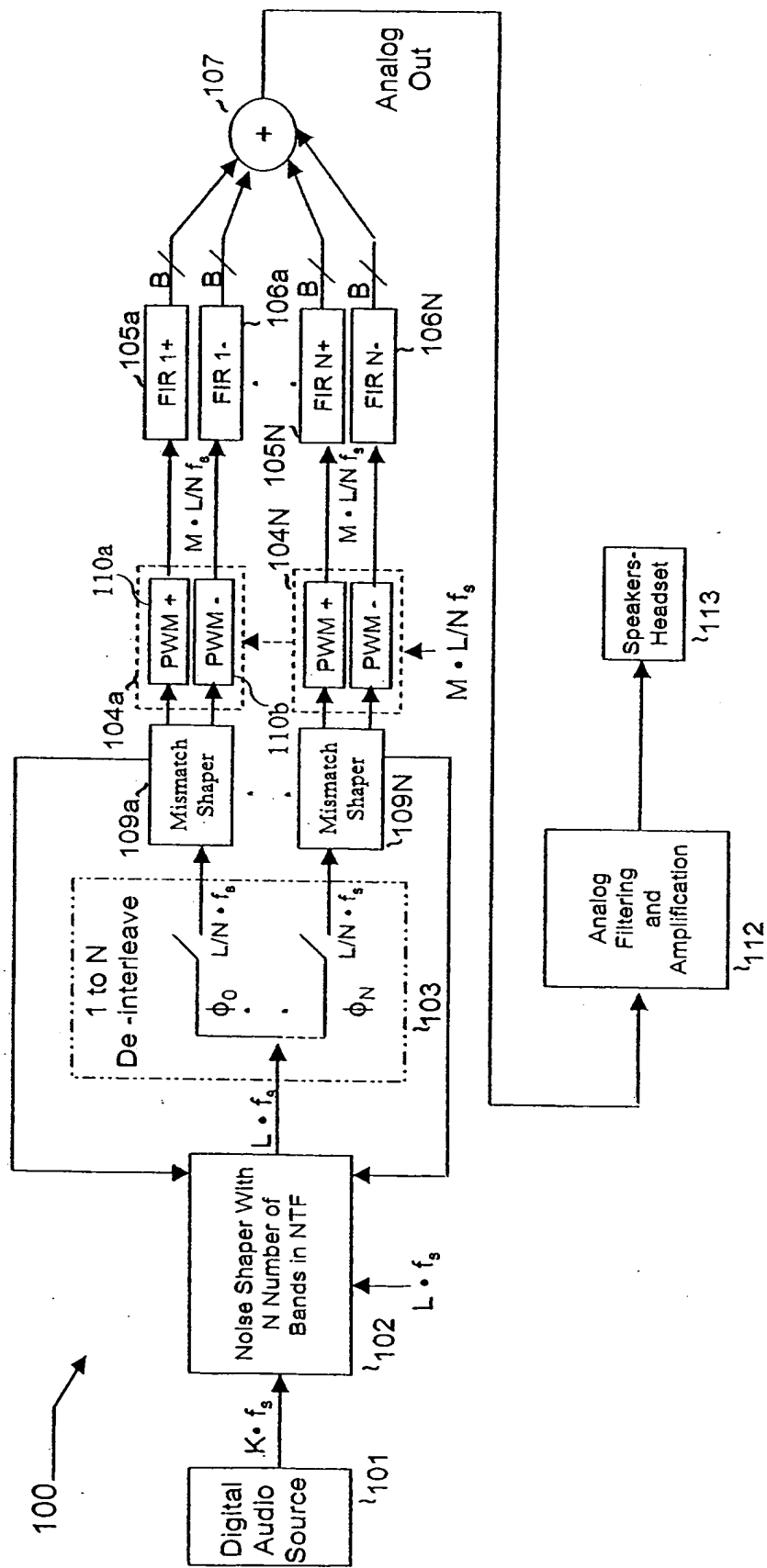


Fig. 1A

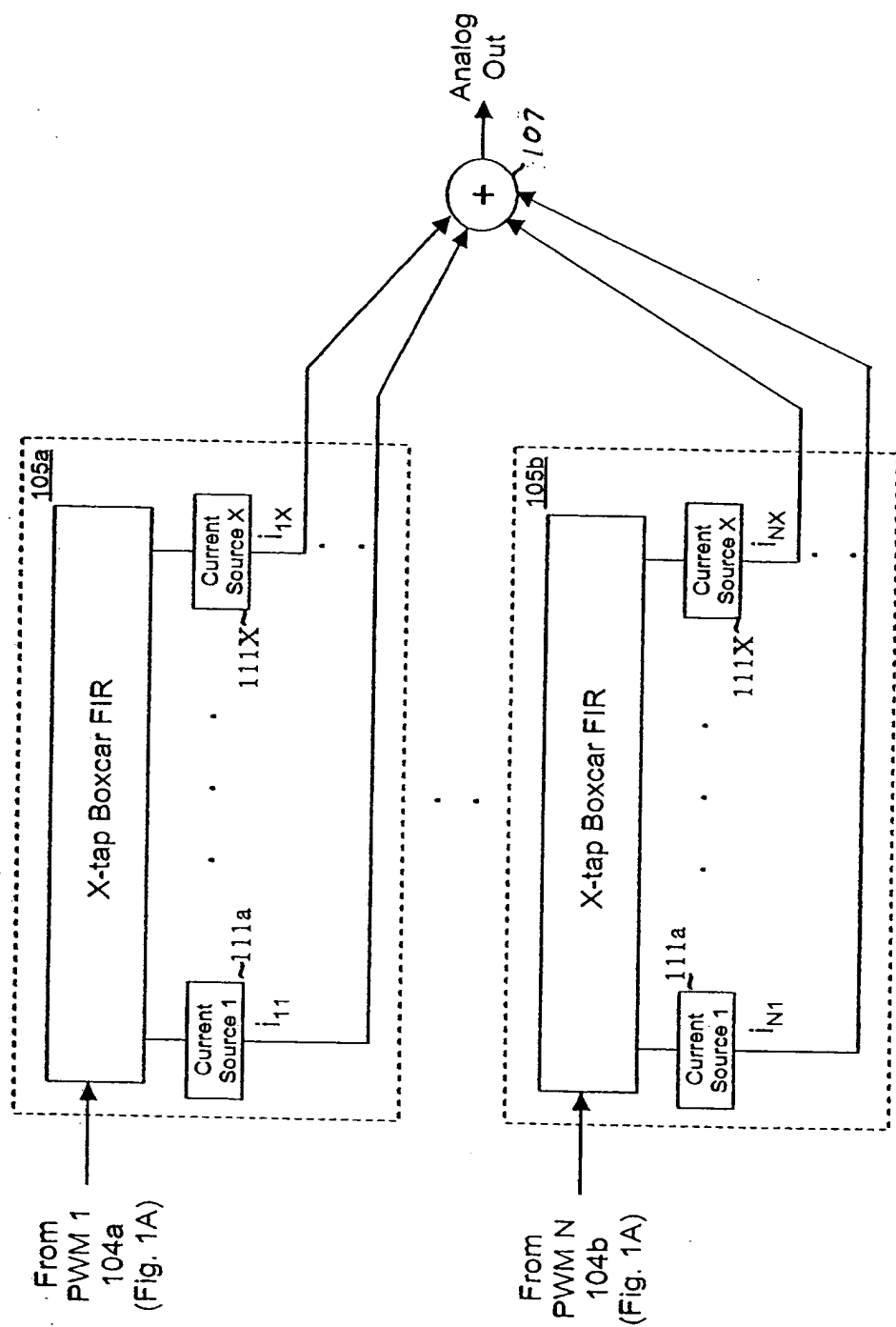


Fig. 1B

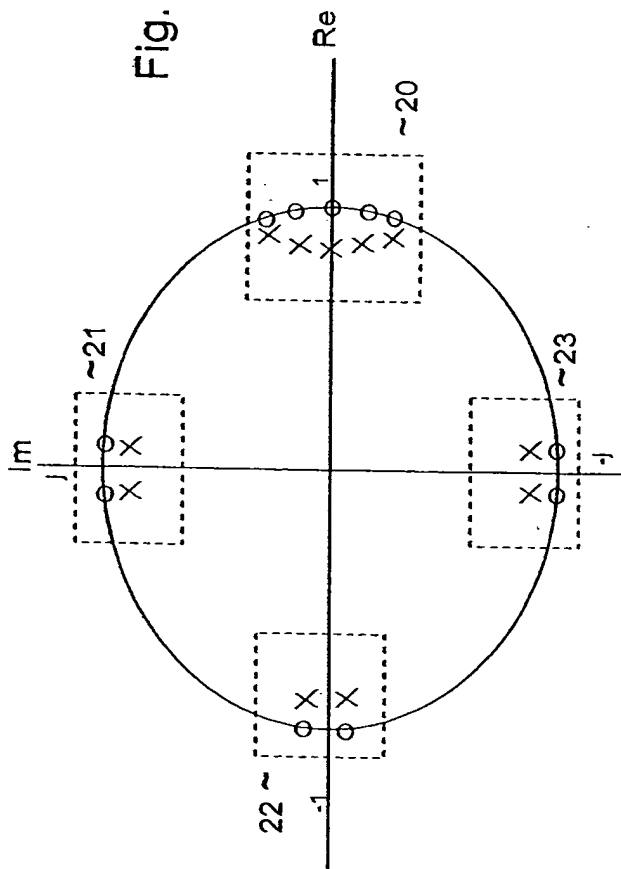
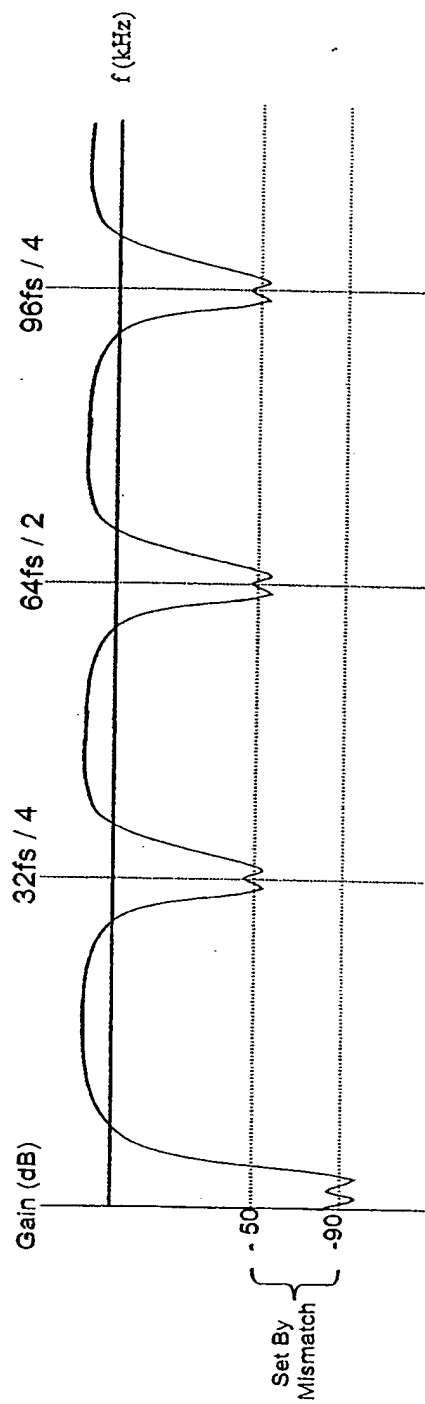


Fig. 2A



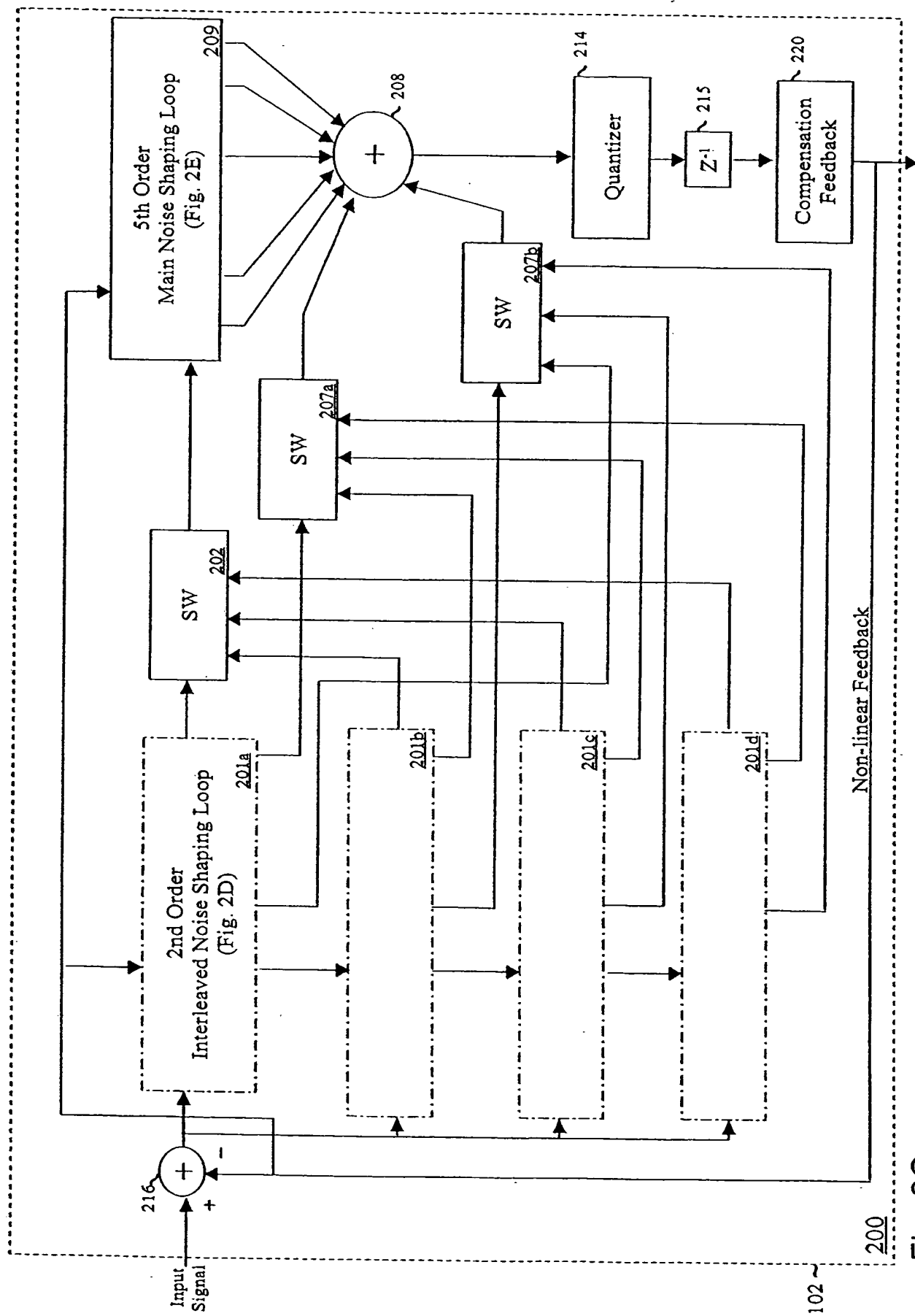
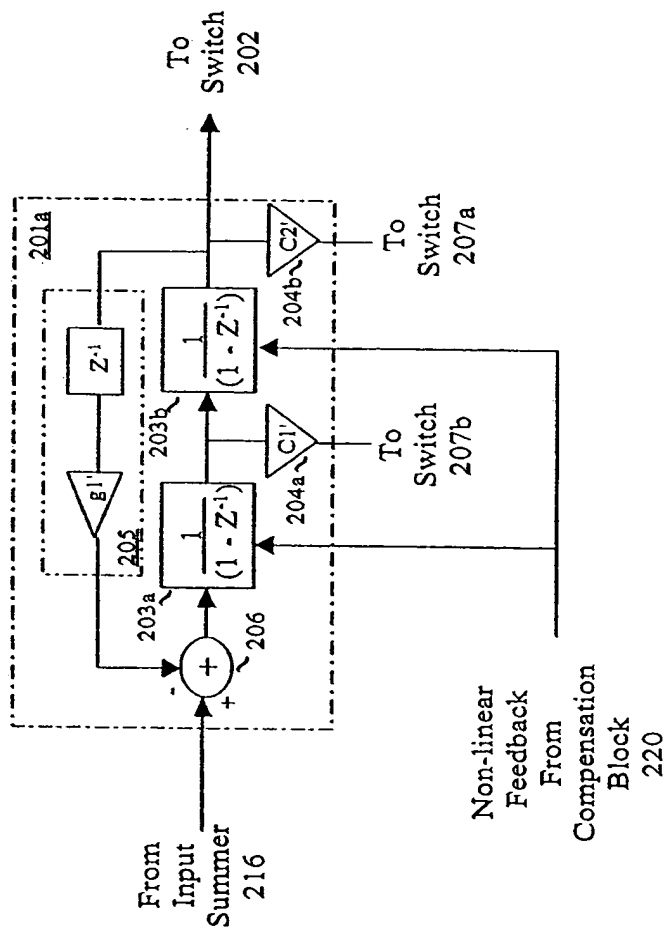


Fig. 2C

Fig. 2D



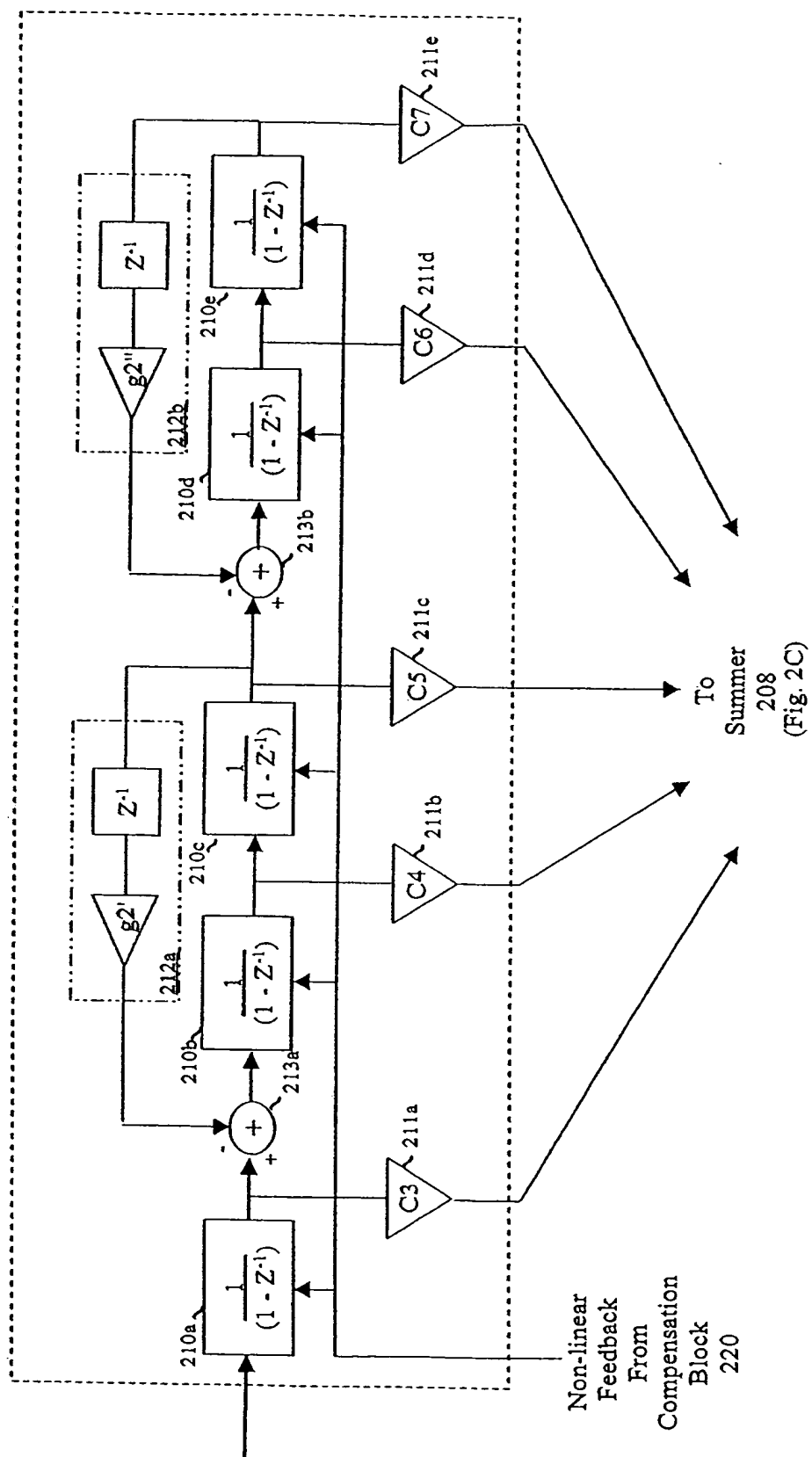


Fig. 2E

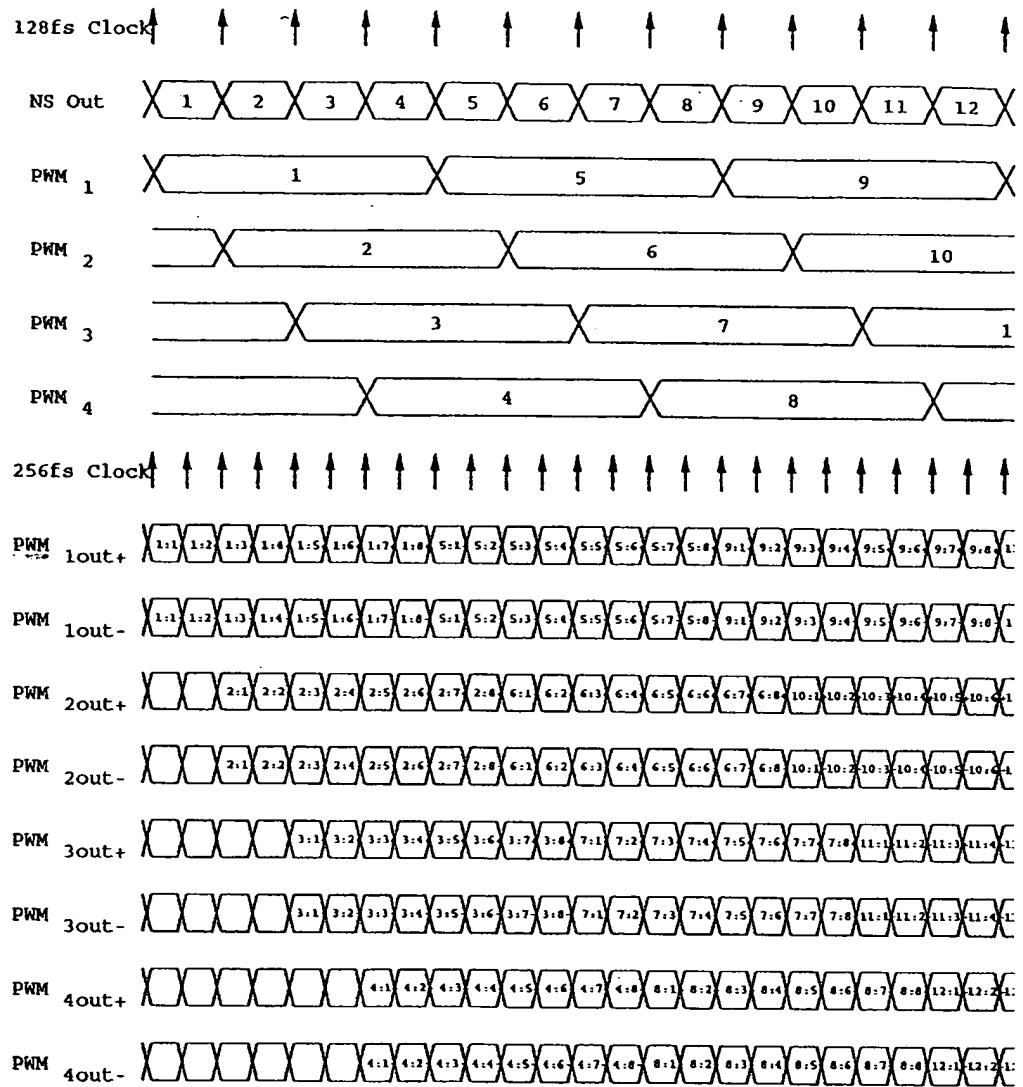


Figure 3

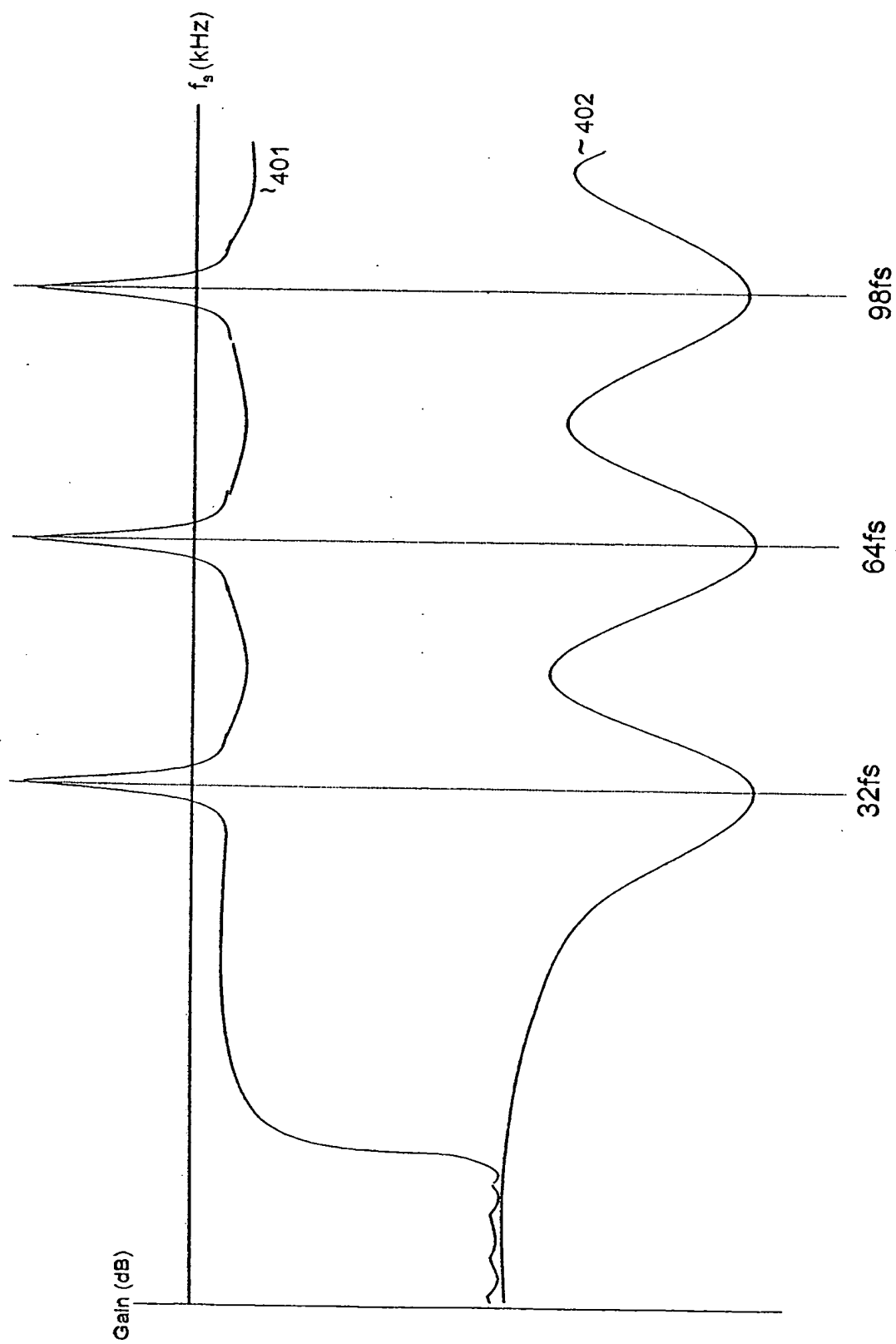


Fig. 4



Figure 5A

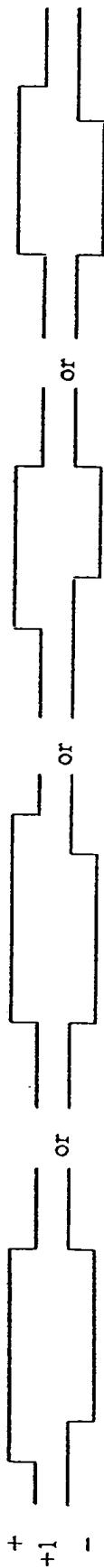


Figure 5B

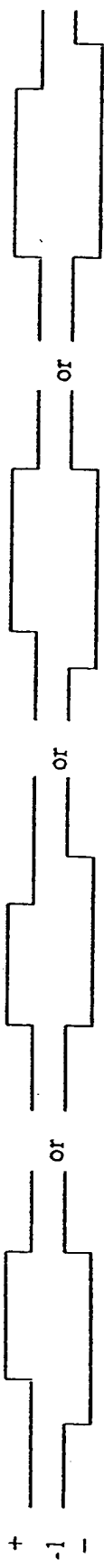


Figure 5C

Fig. 6A



Fig. 6B



Fig. 6C



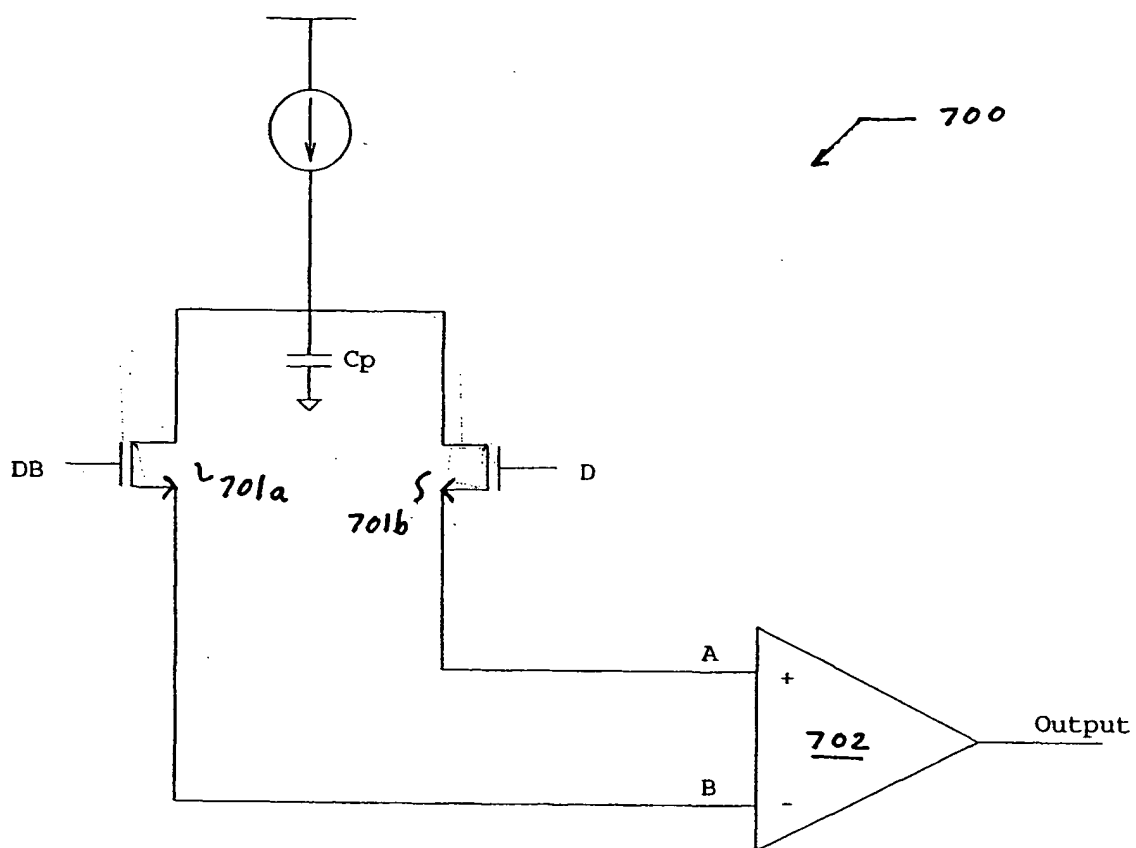


Fig. 7A

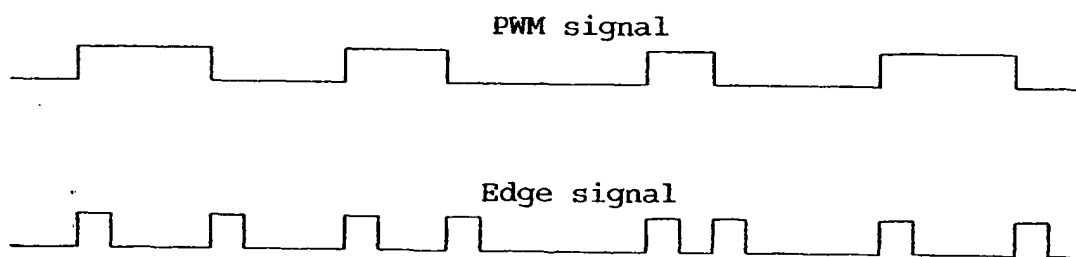


Fig. 7B

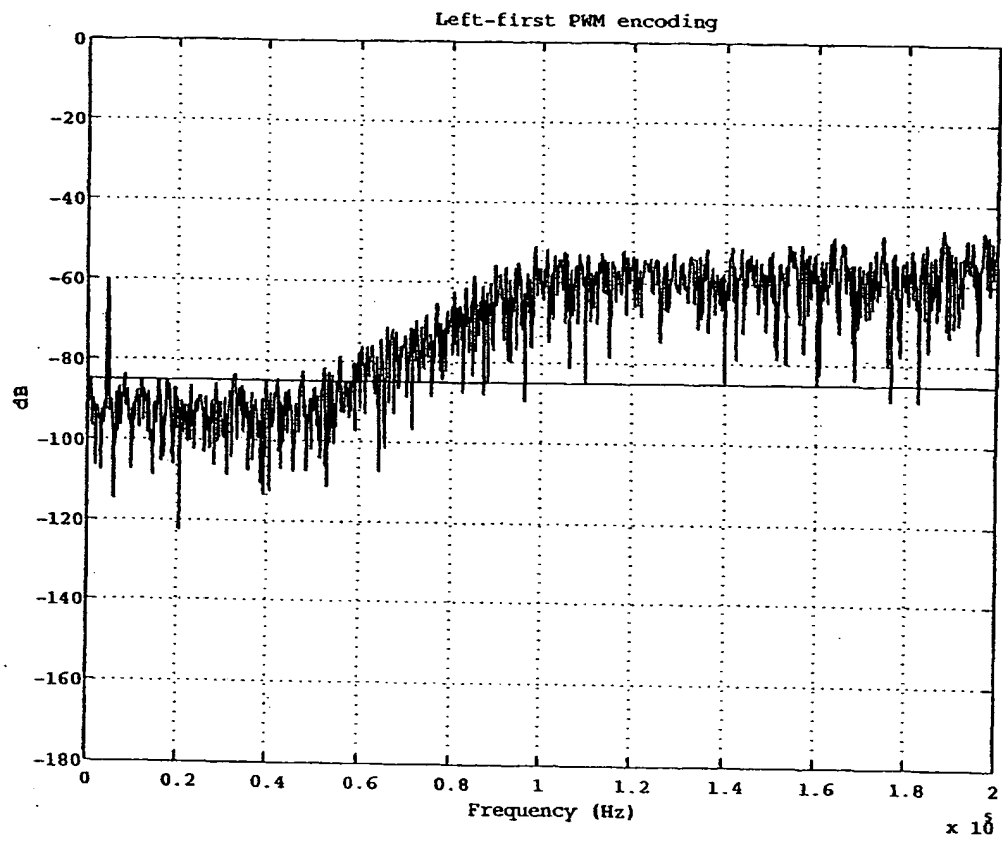


Fig. 7C

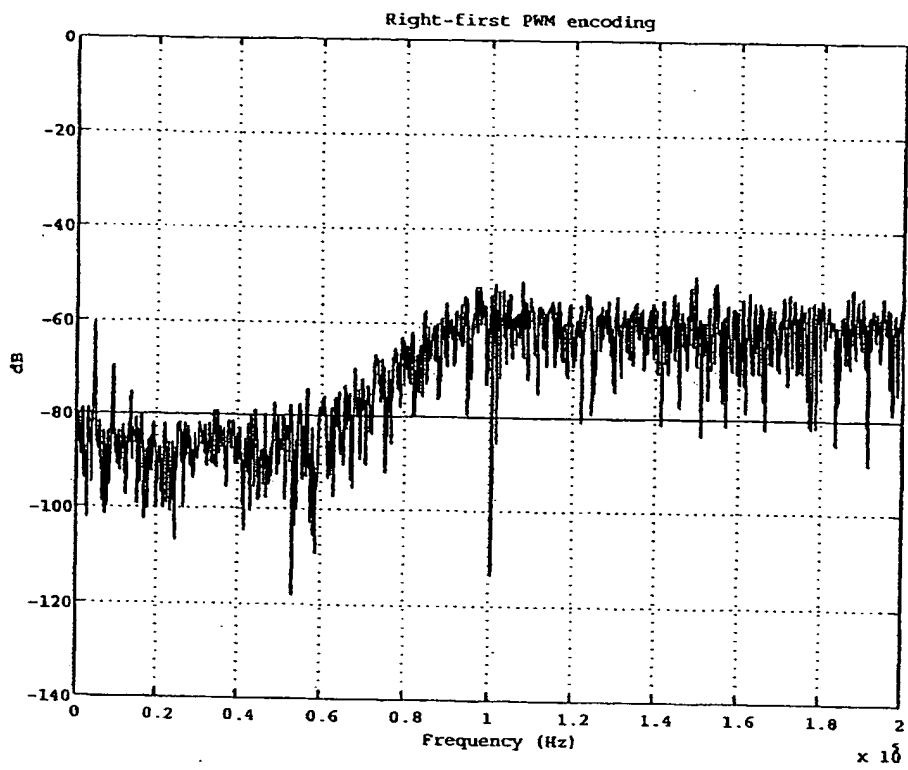


Fig. 7D

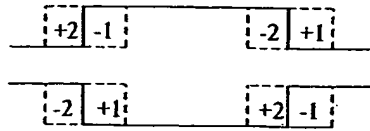


Fig. 8a

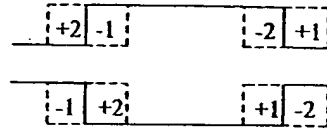


Fig. 8b

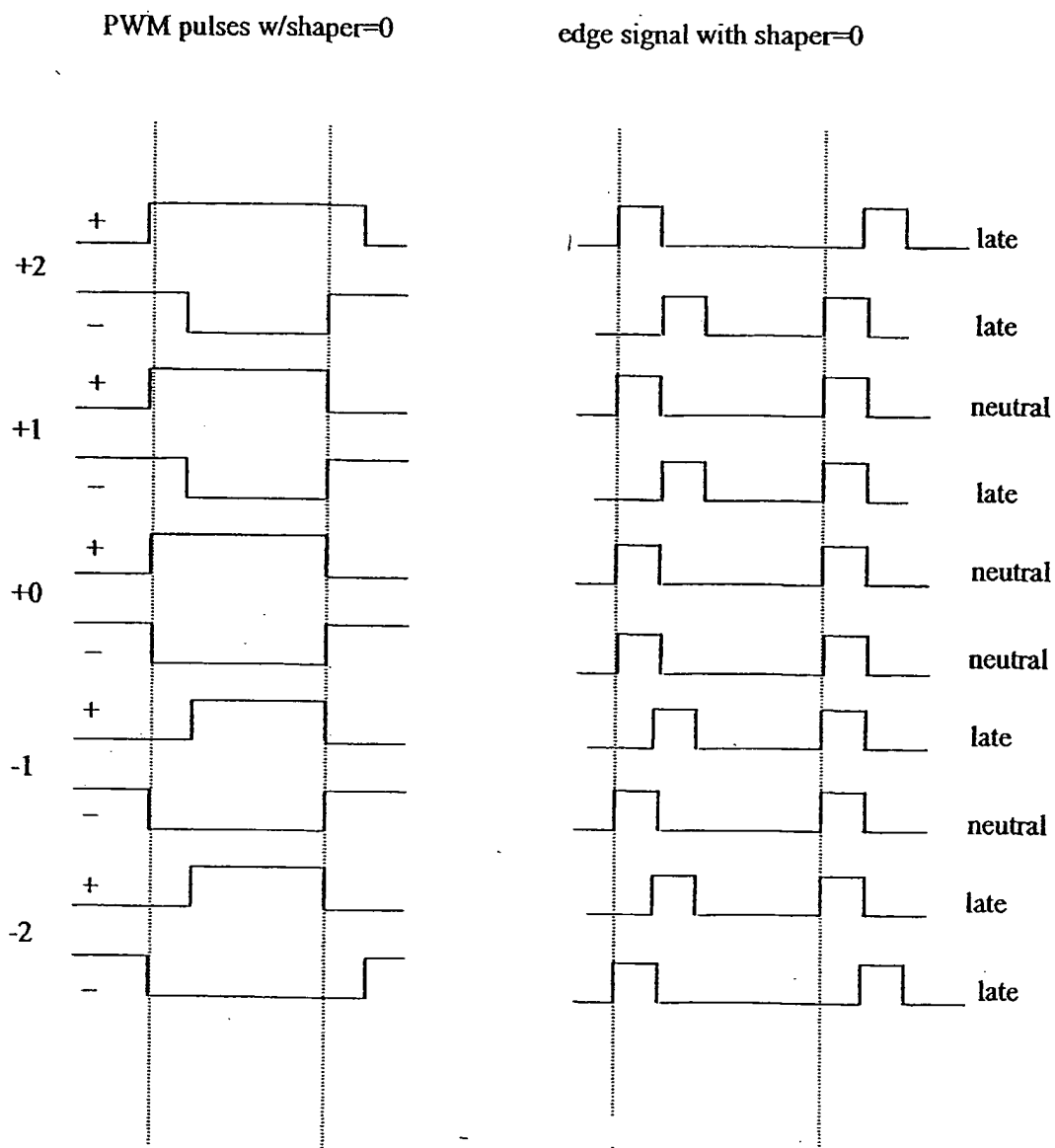


Fig. 9A

PWM pulses w/shaper=0

Edge signal with shaper=0

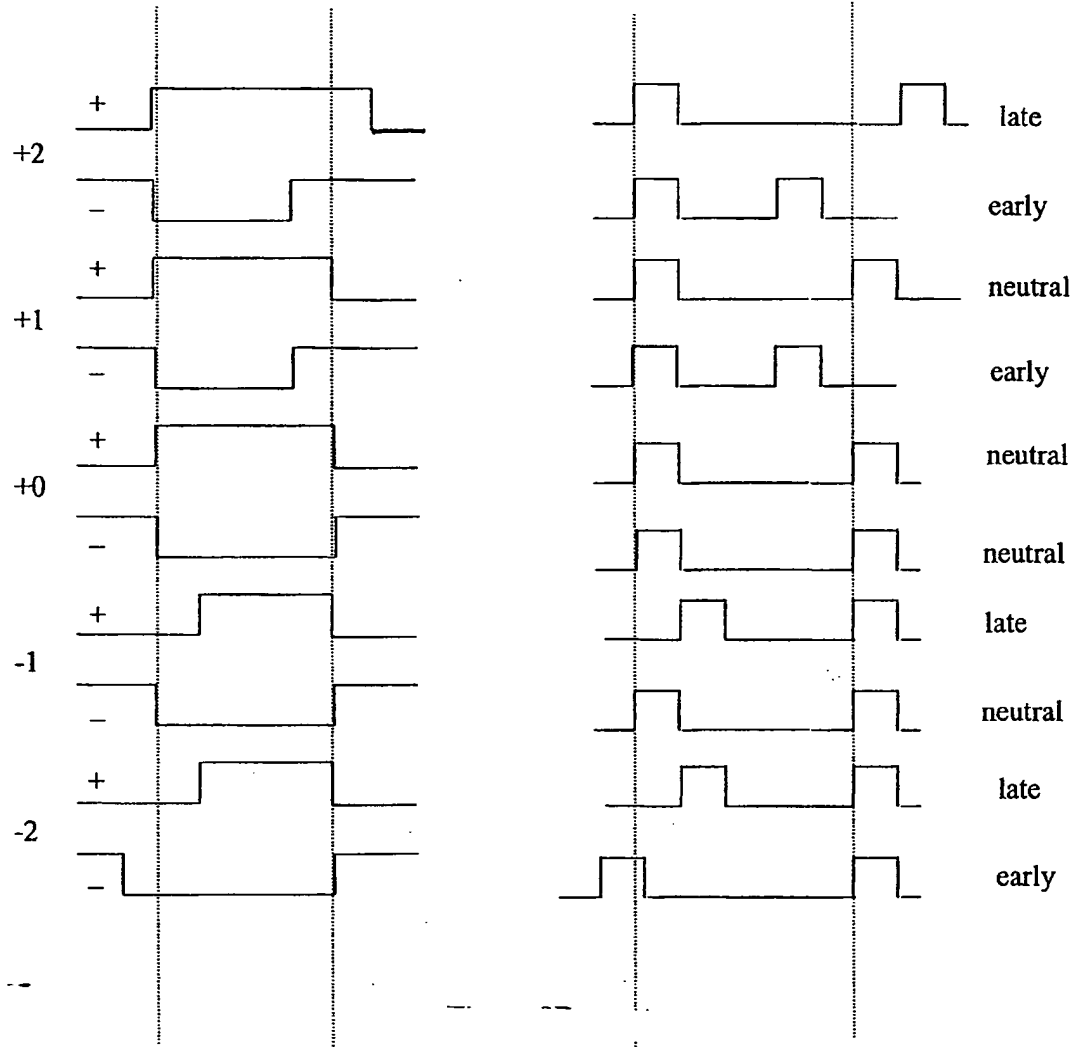
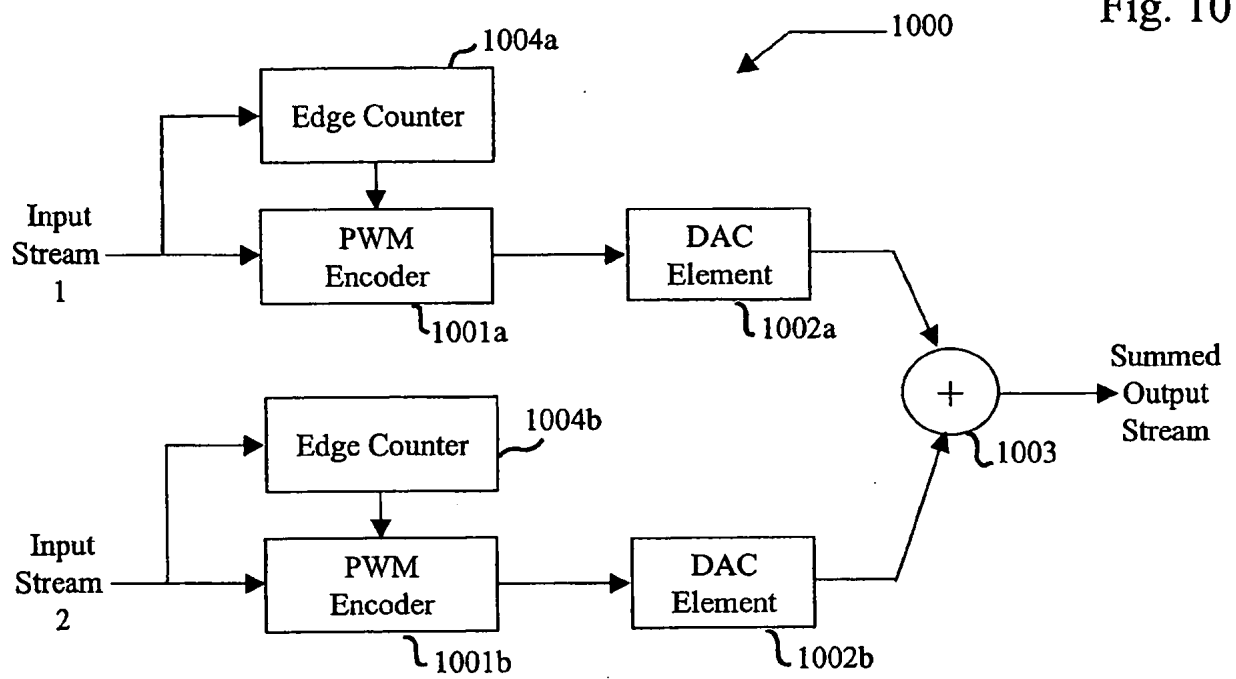


Fig. 9B

Fig. 10



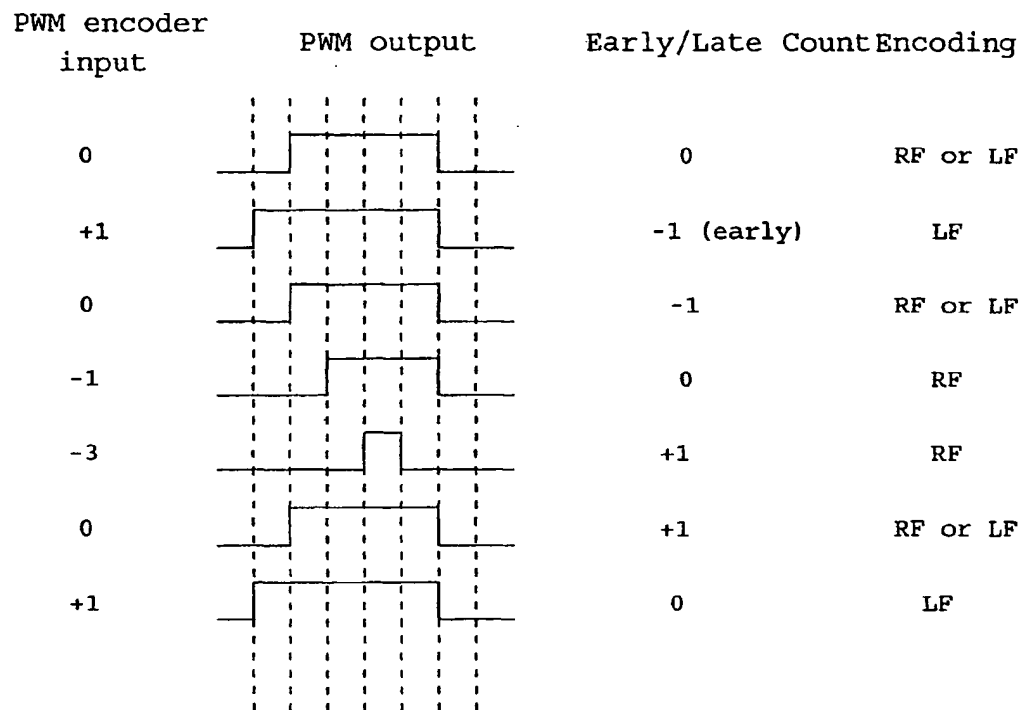


Fig. 11

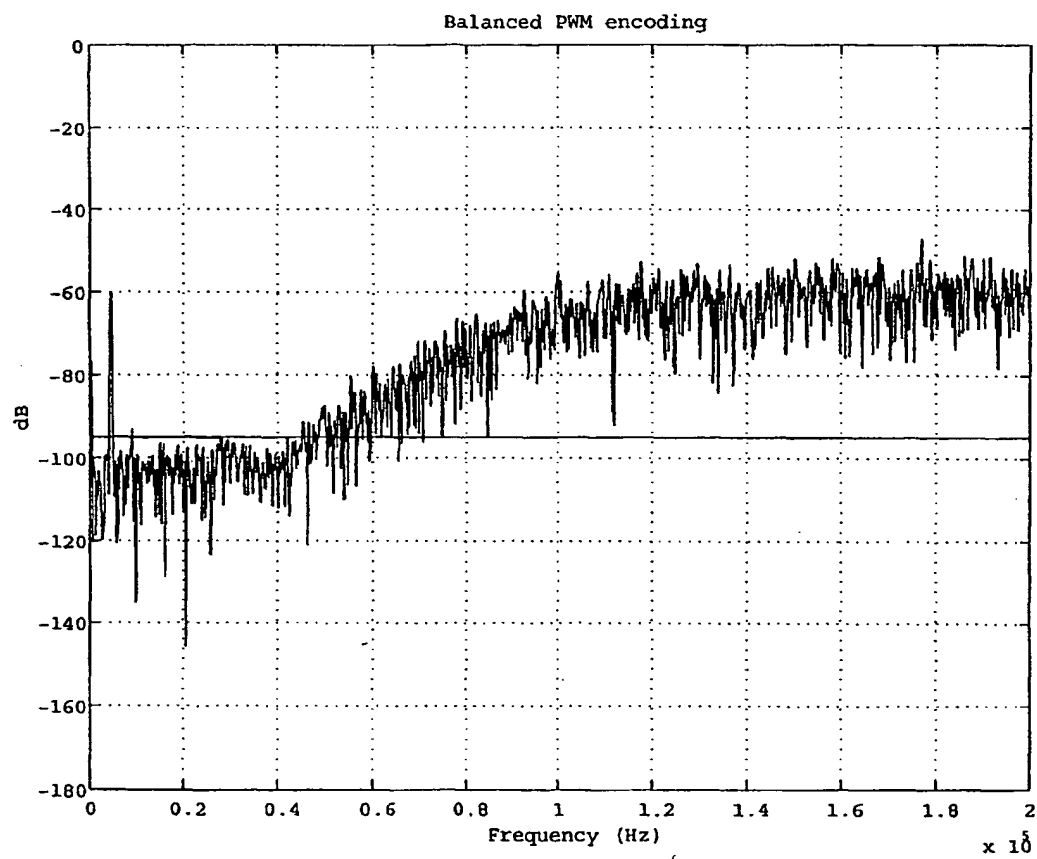


Fig. 12